



JPW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No. 2380

Satoru MIYAGI

Art Unit: 2816

Serial No.: 10/674,016

Examiner: K. Wells

Filed: September 30, 2003

Docket No.: 100021-00133

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING A
LEAKAGE CURRENT CUTOFF CIRCUIT, CONSTRUCTED USING
MT-CMOS, FOR REDUCING STANDBY LEAKAGE CURRENT

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.121

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, Va. 22313-1450

July 19, 2005

Sir:

Responsive to the Office Action dated April 19, 2005, consideration of the following amendment is respectfully requested.

Amendments to the Claims are reflected in a listing of the claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 12 of this paper.